



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of

Docket No.: FIS920030186US1

Dureseti Chidambarao, et al.

Serial No.: 10/667,601

Group Art Unit: 2812

Filed: September 23, 2003

Examiner: Unassigned

For: **IMPROVED NFETS USING GATE INDUCED STRESS MODULATION**

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

SUBMISSION OF FORMAL DRAWINGS

Sir:

Submitted herewith, are ten (10) sheets of formal drawings comprising figures 1-5 for the above-identified patent application. Please substitute the formal drawings for the drawings which were filed with the application.

Respectfully submitted,

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